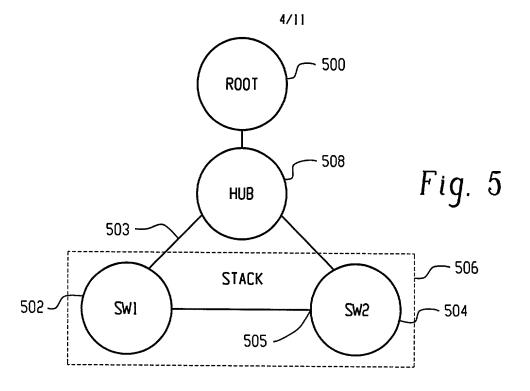


34 — 402		Z 412	414	416	418	422	7-420	424	·	•
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LSA PACKET 7410	LSA_seq_num	LSA_seq_num F HEAD ROUTER SID (FOR DEBUGGING ONLY)	NEIGHBOR NUMBER	ORIGINATOR BRIDGE 103	NEIGHBOR_1 PORT ID	RESERVED	NEIGHBOR_2 PORT ID	RESERVED	•	
400———————————————————————————————————	RESERVED	MASTER SID (FOR DEBUGGING ONLY)	ORIGINATOR SWITCH IO	ORIGINATOR BRIDGE IDI ORIGINATOR BRIDGE IDS ORIGINATOR BRIDGE IDS ORIGINATOR BRIDGE IDS	⊣ ″′	ORIGINATOR PORT 101	NEIGHBOR_2 SWITCH ID	ORIGINATOR PORT 102	•	
		436~MA	434~	416 ORIGINAT	426~	430~	428~	432~		



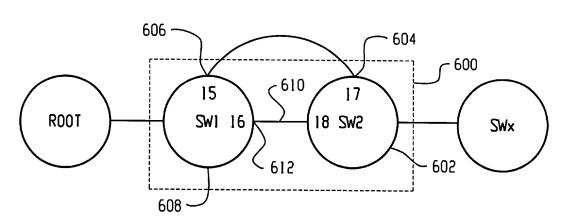


Fig. 6

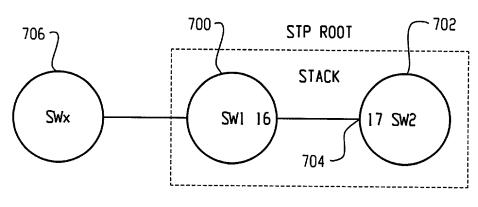
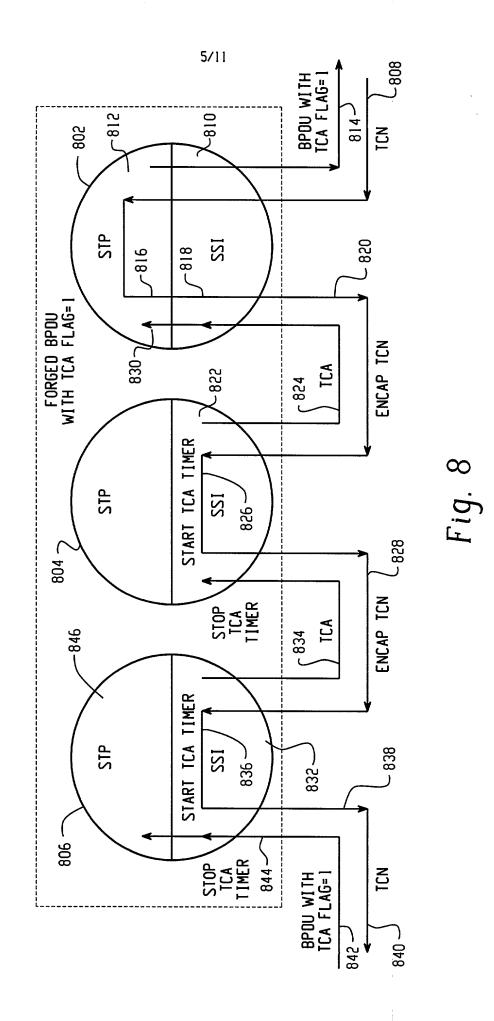
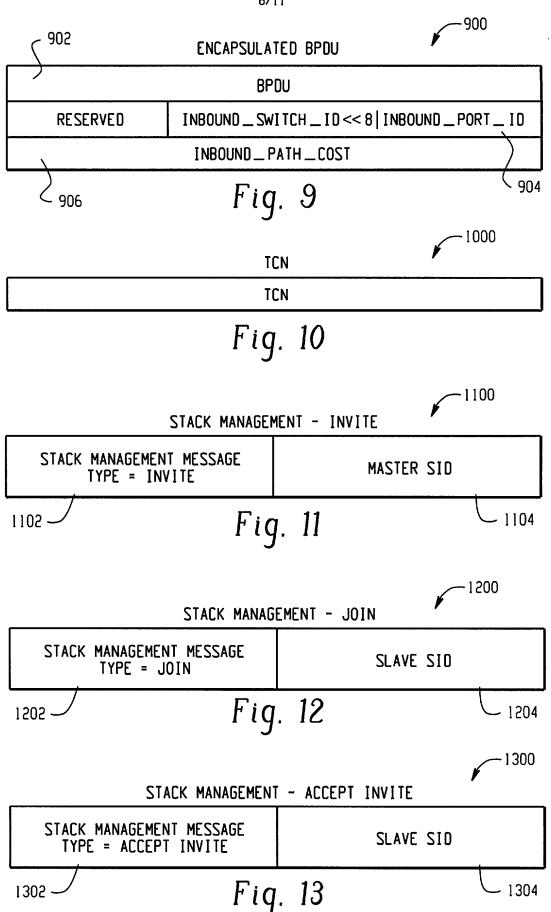


Fig. 7

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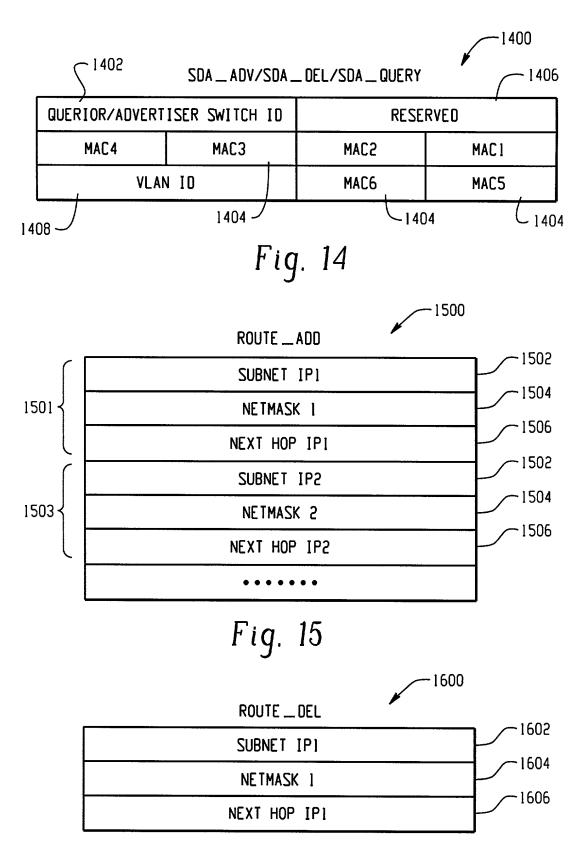
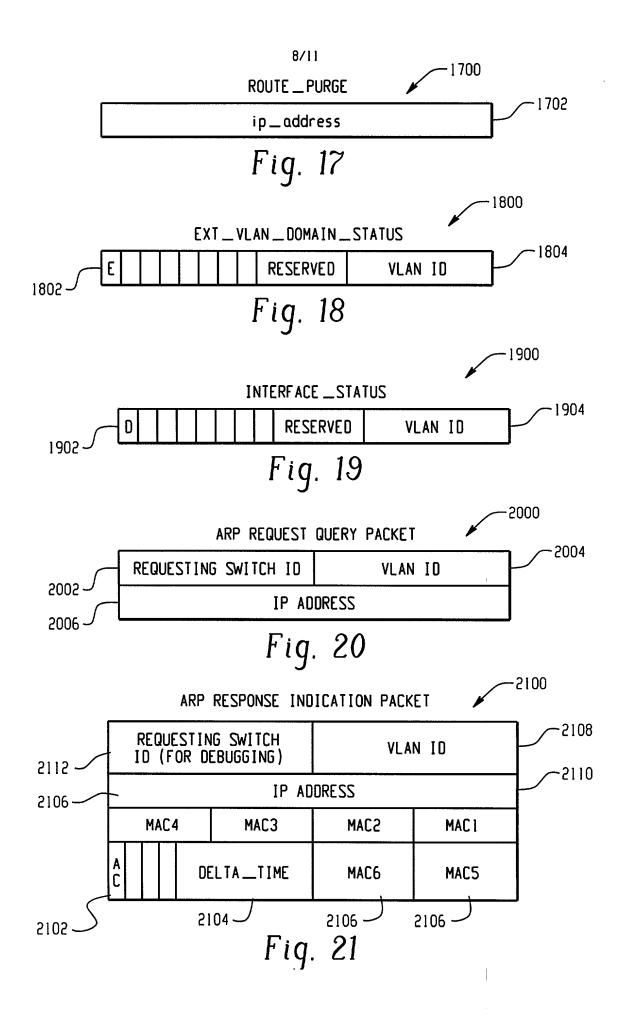


Fig. 16



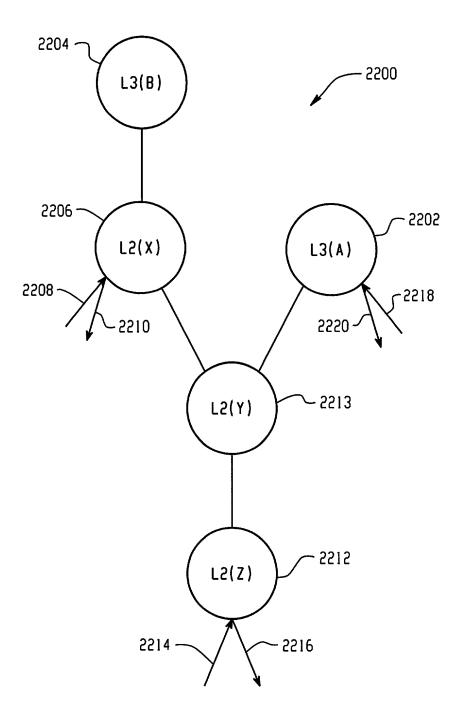


Fig. 22

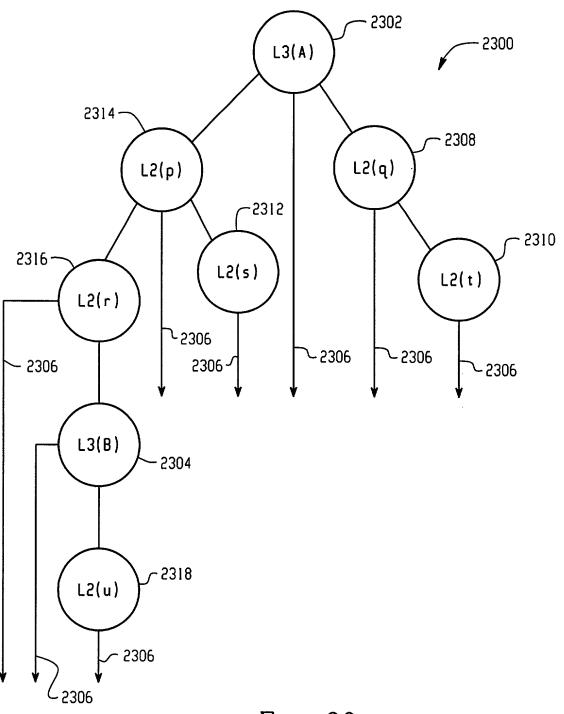


Fig. 23

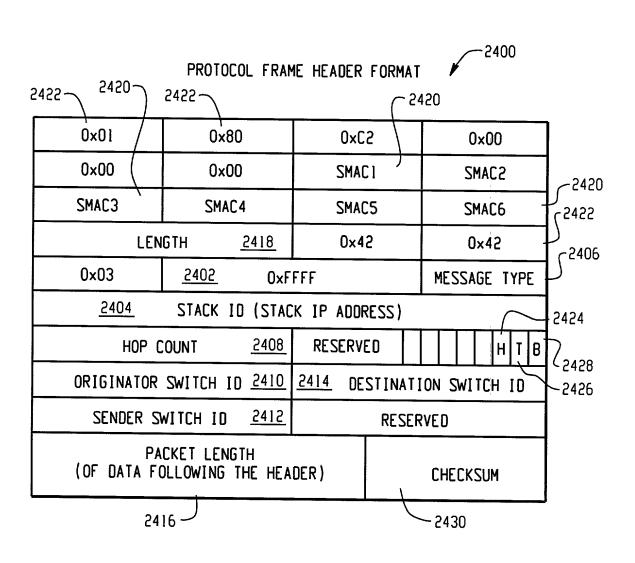


Fig. 24